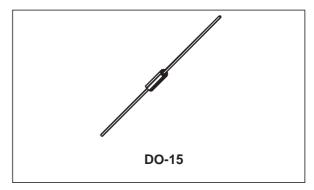


TPA SERIES

FEATURES

- BIDIRECTIONAL CROWBAR PROTECTION.
- VOLTAGE RANGE: FROM 62 V TO 270 V.
- HOLDING CURRENT : I_H = 150mA min.
- REPETITIVE PEAK PULSE CURRENT : I_{PP} = 50 A, 10/1000 μs.

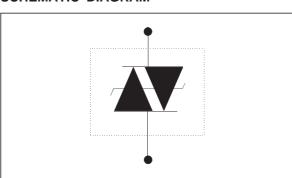


DESCRIPTION

The TPA series are TRISIL devices especially designed for protecting sensitive telecommunication equipment against lightning and transient voltages induced by AC power lines. They are available in the DO-15 axial package.

TRISIL devices provide bidirectional protection by crowbar action. Their characteristic response to transient overvoltages makes them particularly suited to protect voltage sensitive telecommunication equipment.

SCHEMATIC DIAGRAM



COMPLIES WITH THE FOLLOWING STANDARDS:	Peak Surge Voltage (V)	Voltage Waveform (μs)	Current Waveform (μs)	Admissible lpp (A)	Necessary Resistor (Ω)
(CCITT) ITU-K20	1000	10/700	5/310	25	-
(CCITT) ITU-K17	1500	10/700	5/310	38	-
VDE0433	2000	10/700	5/310	50	-
VDE0878	2000	1.2/50	1/20	50	-
IEC-1000-4-5	level 3 level 4	10/700 1.2/50	5/310 8/20	50 100	-
FCC Part 68, lightning surge type A	1500 800	10/160 10/560	10/160 10/560	75 55	12.5 6.5
FCC Part 68, lightning surge type B	1000	9/720	5/320	25	-
BELLCORE TR-NWT-001089 First level	2500 1000	2/10 10/1000	2/10 10/1000	150 50	11.5 10
BELLCORE TR-NWT-001089 Second level	5000	2/10	2/10	150	11.5
CNET I31-24	1000	0.5/700	0.8/310	25	-

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TPA SERIES

ABSOLUTE MAXIMUM RATINGS $(T_{amb} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit	
Р	Power dissipation on infinite heatsink	T _{amb} = 50 °C	1.7	W
Ірр	Peak pulse current	10/1000 μs 8/20 μs	50 100	А
I _{TSM}	Non repetitive surge peak on-state current	tp = 20 ms	30	Α
l ² t	I ² t value for fusing	tp = 20 ms	9	A ² s
dV/dt	Critical rate of rise of off-state voltage	V _{RM}	5	kV/μs
T _{stg} Tj	Storage temperature range Maximum junction temperature		- 55 to + 150 150	ο̈́ο

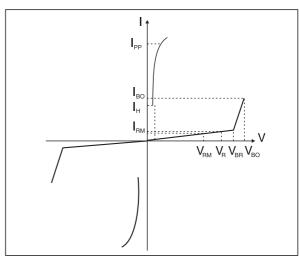
THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
R _{th} (j-l)	Junction to leads (L _{lead} = 10mm)	60	°C/W
R _{th} (j-a)	Junction to ambient on printed circuit (L _{lead} = 10 mm)	100	°C/W

ELECTRICAL CHARACTERISTICS

 $(T_{amb} = 25^{\circ}C)$

Symbol	Parameter				
V_{RM}	Stand-off voltage				
I _{RM}	Leakage current at stand-off voltage				
V _R	Continuous Reverse voltage				
V_{BR}	Breakdown voltage				
V _{BO}	Breakover voltage				
I _H	Holding current				
I _{BO}	Breakover current				
I _{PP}	Peak pulse current				
С	Capacitance				

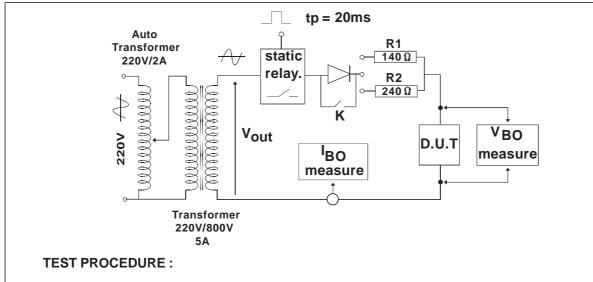


Туре	I _{RM} @ max.	V _{RM}	I _R @ max. note 1	V _R	V _{BO} (max. note 2	@ I _{BO}	I _H min. note 3	C max. note 4
	μ Α	V	μ Α	V	V	mA	mA	pF
TPA62	2	56	50	62	82	800	150	150
TPA68	2	61	50	68	90	800	150	150
TPA100	2	90	50	100	133	800	150	100
TPA120	2	108	50	120	160	800	150	100
TPA130	2	117	50	130	173	800	150	100
TPA180	2	162	50	180	240	800	150	100
TPA200	2	180	50	200	267	800	150	100
TPA220	2	198	50	220	293	800	150	100
TPA240	2	216	50	240	320	800	150	100
TPA270	2	243	50	270	360	800	150	100

 $\begin{array}{ll} \textbf{Note 1:} & I_R \text{ measured at } V_R \text{ guarantee } V_{BRmin} \ \big| \ V_R \\ \textbf{Note 3:} & See \text{ test circuit 2.} \end{array}$

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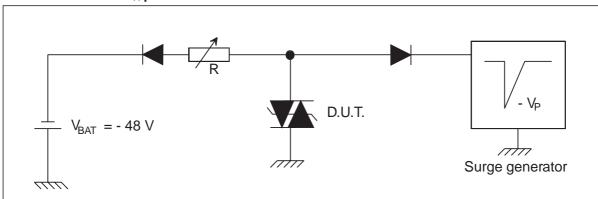
TEST CIRCUIT 1 FOR I_{BO} and V_{BO} parameters :



- Pulse Test duration (tp = 20ms):
 - For Bidirectional devices = Switch K is closed
 - For Unidirectional devices = Switch K is open.
- Vout Selection

 - Device with V_{BO} < 200 Volt Vout = 250 V_{RMS}, R₁ = 140 Ω . Device with V_{BO} | 200 Volt
 - - $V_{OUT} = 480 \text{ V}_{RMS}$, $R_2 = 240 \Omega$.

TEST CIRCUIT 2 for I_H parameter.

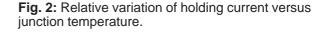


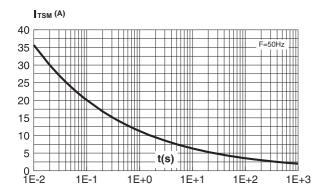
This is a GO-NOGO Test which allows to confirm the holding current (I_H) level in a functional test circuit.

TEST PROCEDURE:

- 1) Adjust the current level at the I_H value by short circuiting the AK of the D.U.T.
 - 2) Fire the D.U.T with a surge Current : Ipp = 10A, $10/1000 \,\mu s$.
 - 3) The D.U.T will come back off-state within 50 ms max.

Fig. 1: Non repetitive surge peak on-state current versus overload duration (Tj initial=25°C).



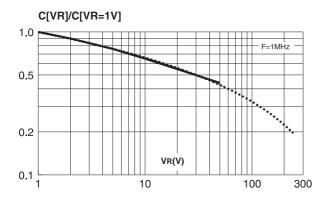


IH[Tj] / IH[Tj=25°C]

2.0
1.8
1.6
1.4
1.2
1.0
0.8
0.6
0.4
0.2
-40 -20 0 20 40 60 80 100

Fig. 3: Relative variation of junction capacitance versus reverse applied voltage (typical values). **Note:** For V_{RM} upper than 56V, the curve is extrapolated (dotted line).

Fig. 4: On-state current versus on-state voltage (typical values).



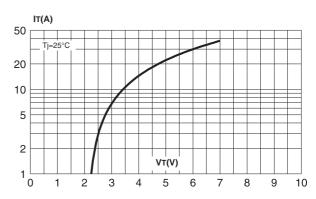


Fig. 5: Transient thermal impedance junction to ambient versus pulse duration (for FR4 PC Board with $T_{lead} = 10$ mm).

Zth(j-a)(°CW)

1E+2

1E+1

1E+0

1E-1

1E-3

1E-2

1E-1

1E+0

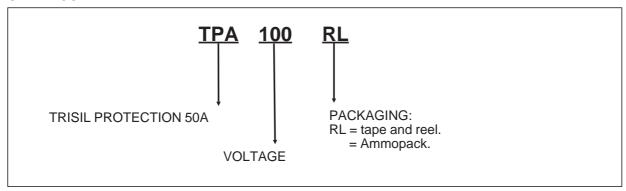
1E+1

1E+2

5E+2

4/5

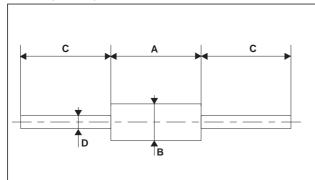
ORDER CODE



MARKING: Logo, Date Code, Part Number.

PACKAGE MECHANICAL DATA

DO-15 (Plastic)



REF.	DIMENSIONS						
	Millim	neters	Inc	hes			
	Min. Max.		Min.	Max.			
Α	6.05	6.75	0.238	0.266			
В	2.95	3.53	0.116	0.139			
С	26	31	1.024	1.220			
D	0.71	0.88	0.028	0.035			

Weight: 0.4 g

Packaging: Standard packaging is in tape and reel.

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